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"trace cache" branch fragmentation Search

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Improving **Trace Cache** Effectiveness with **Branch** Promotion and Trace Packing - group of 8 »
SJPME Yale, N Patt - doi.ieeecomputersociety.org
... may overcome the costs of **fragmentation**, particularly if the ... centage increase is over a **trace cache** which performs ... For all packing schemes, **branch** promotion is ...
Cited by 71 - Web Search - BL Direct

The Block-based **Trace Cache** - group of 19 »
B Black, B Rychlik, JP Shen - ACM SIGARCH Computer Architecture News, 1999 - portal.acm.org
... To study the **fragmentation** for different block sizes, the block-based **trace cache** is simulated with realistic **branch** prediction and a fixed number of block ...
Cited by 53 - Web Search - BL Direct

Dynamo: A transparent dynamic optimization system - group of 28 »
V Bala, E Duesterwald, S Banerjia - PLDI, 2000 - portal.acm.org
... IR itself. After that, a unique exit stub is emitted for every **fragment** exit **branch** and **fragment** loop-back **branch**. The exit stub ...
Cited by 278 - Web Search

Completion time multiple **branch** prediction for enhancing **trace cache** performance - group of 6 »
R Rakvic, B Black, JP Shen - ACM SIGARCH Computer Architecture News, 2000 - portal.acm.org
... traces at fetch time. Section 2 summarizes previous work on multiple **branch** prediction and the **trace cache**. The TMP is presented ...
Cited by 18 - Web Search - BL Direct

Performance limits of trace caches - group of 12 »
M Postiff, G Tyson, TN Mudge - 1998 - jilp.org
... of the **trace cache**. These include **fragmentation**, duplication, indexability, and efficiency met- rics. We show that performance is more limited by **branch** ...
Cited by 16 - View as HTML - Web Search - Library Search

[PS] Design and implementation of a dynamic optimization framework for Windows - group of 6 »
D Bruening, E Duesterwald, S Amarasinghe - 4th ACM Workshop on Feedback-Directed and Dynamic ..., 2000 - catfish.csail.mit.edu
... If there is no such **fragment**, a new basic block **fragment** is created. ... **TRACE CACHE** non−control−flow instructions ... trace **branch** taken? 6 instructions START ...
Cited by 44 - View as HTML - Web Search

eXtended Block Cache - group of 8 »
S Jourdan, L Rappoport, Y Almog, M Erez, A Yoaz, R ... - The 6 th International Symposium on High-Performance ..., 2000 - doi.ieeecomputersociety.org
... hit rate is slightly reduced (due to **fragmentation**). ... **Trace Cache** The **trace cache** (TC) aims to reduce ... is required due to the limited **branch** prediction bandwidth ...
Cited by 12 - Web Search

A Scalable Front-End Architecture for Fast Instruction Delivery - group of 22 »
G Reinman, T Austin, B Calder - ACM SIGARCH Computer Architecture News, 1999 - portal.acm.org
... Attaining these targets is a challenging task due to I-cache misses, **branch** mispredictions, and taken branches in the instruction stream. ...
Cited by 65 - Web Search - BL Direct

[PS] **Trace cache** design for wide-issue superscalar processors - group of 2 »
SJ Patel - 1999 - crhc.uiuc.edu
... of two important performance limitations of the **trace cache**: **branch** resolution time and ... 125 9.2 **Trace Cache** with Promotion, 1 **Branch** Only . . . .
Cited by 6 - View as HTML - Web Search - Library Search

An infrastructure for adaptive dynamic optimization - group of 19 »
D Bruening, T Garnett, S Amarasinghe - International Symposium on Code Generation and Optimization, 2003 - ieeexplore.ieee.org
... is called each time a **fragment** is deleted from the block or **trace cache**. ... thread will continue to execute in the old **fragment** only until the next **branch**. ...
Cited by 67 - Web Search

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Scholar Results **1 - 10** of about **591** for **"trace cache" "branch prediction"**. (**0.19** seconds)

Trace Cache: A Low Latency Approach to High Bandwidth Instruction Fetching - group of 55 »
E Rotenberg, S Bennett, JE Smith - MICRO-ANNUAL WORKSHOP THEN ANNUAL INTERNATIONAL SYMPOSIUM-, 1996 - doi.ieeecs.org
... Alternatively, the index into the **trace cache** could be derived by concatenating the fetch address with the **branch prediction** bits. ...
Cited by 335 - Web Search - Library Search - BL Direct

Improving **Trace Cache** Effectiveness with Branch Promotion and Trace Packing - group of 8 »
SJPME Yale, N Patt - doi.ieeecomputersociety.org
... **cache** is being used for, say, an 8-wide machine, then promotion opens the possibility of using aggressive hybrid single **branch prediction** with the **trace cache**. ...
Cited by 71 - Web Search - BL Direct

Optimization of Instruction Fetch Mechanisms for High Issue Rates - group of 3 »
TM Conte, KN Menezes, PM Mills, BA Patel - ANNUAL INTERNATIONAL SYMPOSIUM ON COMPUTER ARCHITECTURE, 1995 - portal.acm.org
... Accurate **branch prediction** and low I-cache miss ratios are essential for the efficient operation of the fetch unit. Several studies ...
Cited by 126 - Web Search - BL Direct

Completion time multiple **branch prediction** for enhancing **trace cache** performance - group of 6 »
R Rakvic, B Black, JP Shen - ACM SIGARCH Computer Architecture News, 2000 - portal.acm.org
... traces at fetch time. Section 2 summarizes previous work on multiple **branch prediction** and the **trace cache**. The TMP is presented ...
Cited by 18 - Web Search - BL Direct

Putting the Fill Unit to Work: Dynamic Optimizations for **Trace Cache** Microprocessors - group of 4 »
DH Friendly, SJ Patel, YN Patt - MICRO-ANNUAL WORKSHOP THEN ANNUAL INTERNATIONAL SYMPOSIUM-, 1998 - portal.acm.org
... Like dy- namic **branch prediction** and out-of-order execution, dyan- mic trace ... of this work is the initial research per- formed on the **trace cache** by several ...
Cited by 88 - Web Search - BL Direct

Branch history table prediction of moving target branches due to subroutine returns - group of 3 »
DR Kaeli, PG Emma - Proc. 18th Annual International Symposium on Computer ..., 1991 - portal.acm.org
... 198(l. 9 James E. Smith, A study of **branch prediction** strategies, Proceedings of the 8th annual symposium on Computer Architecture, p.135-148, May 12-14, 1981 ...
Cited by 100 - Web Search

Branch Prediction, Instruction-Window Size, and Cache Size: Performance Trade-Offs and

Simulation ... - group of 9 »
K Skadron, PS Ahuja, M Martonosi, DW Clark - IEEE Transactions on Computers, 1999 - swww.ee.uwa.edu.au
Page 1. **Branch Prediction**, Instruction-Window Size, and Cache Size: Performance ... These benchmarks all have **branch prediction** accuracy below 95%. ...
Cited by 68 - View as HTML - Web Search

Path-Based Next Trace Prediction - group of 24 »
Q Jacobson, E Rotenberg, JE Smith - MICRO-ANNUAL WORKSHOP THEN ANNUAL INTERNATIONAL SYMPOSIUM-, 1997 - doi.ieeecomputersociety.org
... For accessing the **trace cache** we use the following method ... that this limit on branches is not required to simplify simultaneous multiple **branch prediction**, as is ...
Cited by 97 - Web Search - BL Direct

Checkpoint repair for out-of-order execution machines - group of 4 »
WW Hwu, YN Patt - Proceedings of the 14th Annual International Symposium on ..., 1987 - portal.acm.org
... ABSTRACT Out-of-order execution and **branch prediction** are two mechanisms that can be used profitably in the design of Supercomputers to increase performance. ...
Cited by 120 - Web Search

Achieving high instruction cache performance with an optimizing compiler - group of 3 »
WW Hwu, PP Chang - Proceedings of the 16th annual international symposium on ..., 1989 - portal.acm.org
Google, Inc. Subscribe (Full Service), Register (Limited Service, Free), Login. Search: The ACM Digital Library The Guide. ...
Cited by 144 - Web Search

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	10098	("712").CLAS.	USPAT; USOCR	OR	OFF	2006/04/05 14:48
S2	256	(712/237).CCLS.	USPAT; USOCR	OR	OFF	2006/04/05 14:48
S3	363	trace adj cache	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/05 18:11
S5	88	S1 and S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/05 15:02
S7	679	(select or choose) near2 (trace$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/05 15:03
S8	19	S7 and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/05 17:44
S10	364	trace adj cache	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 09:28
S11	162	S10 and (branch adj prediction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 09:29
S12	75	S10 and (branch adj predictor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 09:52

EAST Search History

S13	534	multiple adj traces	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 11:42
S18	364	trace adj cache	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/10 14:09
S19	276	S18 and branch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/10 14:12
S22	64	"trace cache" and "Branch history"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/11 12:12
S23	57	S22 and "712".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/11 12:04
S24	20	"trace cache" same "Branch history"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/11 15:18